ABSTRACT

The present invention is intended to provide a Doherty amplifier that can accomplish a linear amplifying and a power combining operation closer to the ideal in a simple configuration. The configuration comprises gain compensator 6 that is composed of a parallel circuit made of an anti-parallel diode and a resistor that is disposed at a position ahead of peak amplifier 4 included in the Doherty amplifier. Setting the gain in gain compensator 6 allows peak amplifier 4 to be compensated for the operation characteristic, when peak amplifier 4 is operating, based on the gm characteristic of peak amplifier 4.

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